

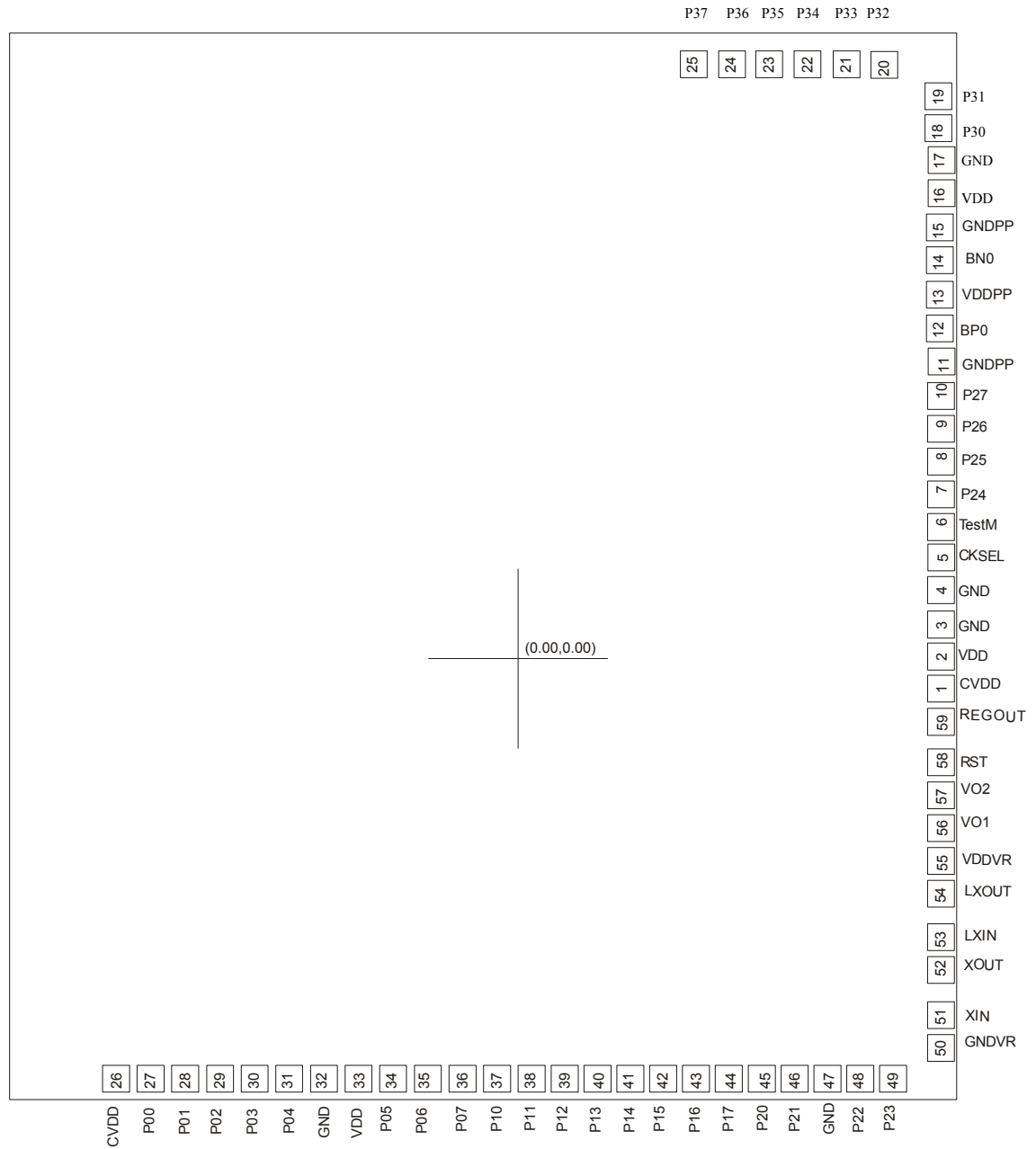


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**PROJECT: SNC88681A**

NO	PAD NAME	X (um)	Y (um)	NO	PAD NAME	X (um)	Y (um)
1	CVDD	1943.00	-883.72	31	P04	-1354.14	-2313.00
2	VDD	1943.00	-747.58	32	GND	-1244.14	-2313.00
3	GND	1943.00	-637.58	33	VDD	-1134.14	-2313.00
4	GND	1943.00	-527.58	34	P05	-1024.14	-2313.00
5	CKSEL	1943.00	-417.58	35	P06	-914.14	-2313.00
6	TestM	1943.00	-307.57	36	P07	-804.14	-2313.00
7	P24	1943.00	-197.57	37	P10	-694.14	-2313.00
8	P25	1943.00	-87.57	38	P11	-584.14	-2313.00
9	P26	1943.00	22.43	39	P12	-474.14	-2313.00
10	P27	1943.00	132.43	40	P13	-364.14	-2313.00
11	GNDPP	1910.00	851.02	41	P14	-254.14	-2313.00
12	BP0	1910.00	961.02	42	P15	-144.14	-2313.00
13	VDDPP	1910.00	1121.72	43	P16	-34.14	-2313.00
14	BN0	1910.00	1282.42	44	P17	75.86	-2313.00
15	GNDPP	1910.00	1392.42	45	P20	185.86	-2313.00
16	VDD	1943.00	1718.18	46	P21	295.86	-2313.00
17	GND	1943.00	1828.18	47	GND	405.86	-2313.00
18	P30	1943.00	1938.18	48	P22	515.86	-2313.00
19	P31	1943.00	2048.18	49	P23	625.86	-2313.00
20	P32	1883.01	2315.00	50	GNDVR	1943.00	-2271.02
21	P33	1773.01	2315.00	51	XIN	1943.00	-2161.02
22	P34	1663.01	2315.00	52	XOUT	1943.00	-2009.42
23	P35	1553.01	2315.00	53	LXIN	1943.00	-1899.42
24	P36	1443.01	2315.00	54	LXOUT	1943.00	-1753.82
25	P37	1333.01	2315.00	55	VDDVR	1943.00	-1643.82
26	CVDD	-1904.14	-2313.00	56	VO1	1943.00	-1533.82
27	P00	-1794.14	-2313.00	57	VO2	1943.00	-1423.82
28	P01	-1684.14	-2313.00	58	RST	1943.00	-1229.10
29	P02	-1574.14	-2313.00	59	REGOUT	1943.00	-990.80
30	P03	-1464.14	-2313.00				



## SNC88681A

CHIP SIZE: X=3990um,Y=4730um

**Note: The substrate MUST be connected to GND in PCB layout**