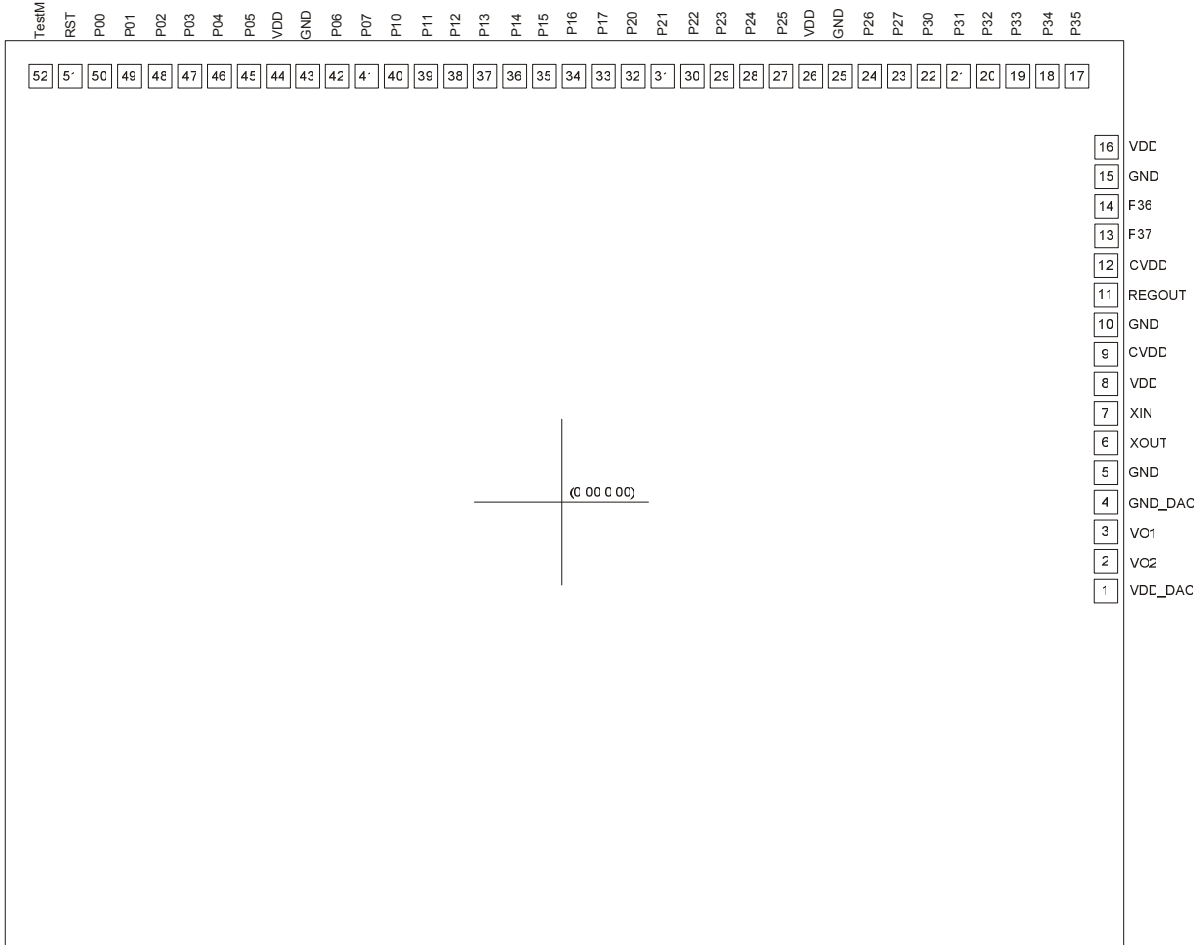




PROJECT: SNC88341B

NO	PAD NAME	X (um)	Y (um)	NO	PAD NAME	X (um)	Y (um)
1	VDD_DAC	1557.00	-201.88	27	P25	600.15	1446.50
2	VO2	1557.00	-121.88	28	P24	520.15	1446.50
3	VO1	1557.00	-41.88	29	P23	440.15	1446.50
4	GND_DAC	1557.00	38.12	30	P22	360.15	1446.50
5	GND	1557.00	118.12	31	P21	280.15	1446.50
6	XOUT	1557.00	213.12	32	P20	200.15	1446.50
7	XIN	1557.00	323.12	33	P17	120.15	1446.50
8	VDD	1557.00	442.94	34	P16	40.15	1446.50
9	CVDD	1557.00	522.94	35	P15	-39.85	1446.50
10	GND	1557.00	602.95	36	P14	-119.85	1446.50
11	REGOUT	1557.00	694.06	37	P13	-199.85	1446.50
12	CVDD	1557.00	801.26	38	P12	-279.85	1446.50
13	P37	1557.00	902.95	39	P11	-359.85	1446.50
14	P36	1557.00	982.95	40	P10	-439.85	1446.50
15	GND	1557.00	1062.95	41	P07	-519.85	1446.50
16	VDD	1557.00	1142.95	42	P06	-599.85	1446.50
17	P35	1400.15	1446.50	43	GND	-679.85	1446.50
18	P34	1320.15	1446.50	44	VDD	-759.85	1446.50
19	P33	1240.15	1446.50	45	P05	-839.85	1446.50
20	P32	1160.15	1446.50	46	P04	-919.85	1446.50
21	P31	1080.15	1446.50	47	P03	-999.85	1446.50
22	P30	1000.15	1446.50	48	P02	-1079.85	1446.50
23	P27	920.15	1446.50	49	P01	-1159.85	1446.50
24	P26	840.15	1446.50	50	P00	-1239.85	1446.50
25	GND	760.15	1446.50	51	RST	-1448.50	1446.50
26	VDD	680.15	1446.50	52	TestM	-1528.50	1446.50



SNC88341B

CHIP SIZE: X=3200um, Y=2979um

Note: The substrate MUST be connected to GND in PCB layout.