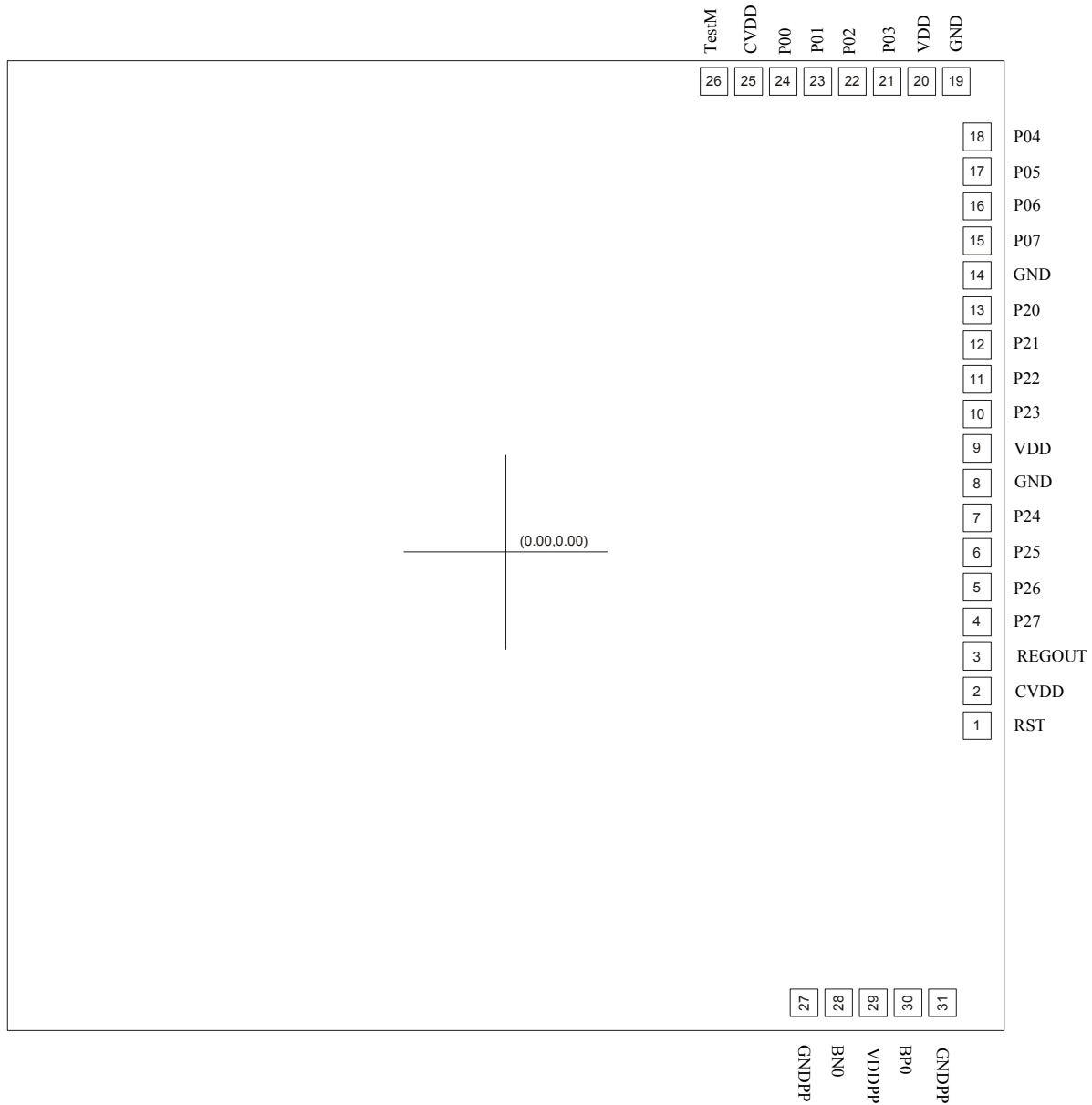




PROJECT: SNC82040B

NO	PAD NAME	X (um)	Y (um)	NO	PAD NAME	X (um)	Y (um)
1	RST	782.00	-158.80	25	CVDD	10.72	1317.80
2	CVDD	782.00	-57.11	26	TestM	-69.28	1317.80
3	REGOUT	782.00	50.09	27	GNDPP	162.75	-1308.50
4	P27	782.00	141.20	28	BN0	272.75	-1308.50
5	P26	782.00	221.20	29	VDDPP	433.45	-1308.50
6	P25	782.00	301.20	30	BP0	594.15	-1308.50
7	P24	782.00	381.20	31	GNDPP	704.15	-1308.50
8	GND	782.00	461.20				
9	VDD	782.00	541.20				
10	P23	782.00	621.20				
11	P22	782.00	701.20				
12	P21	782.00	781.20				
13	P20	782.00	861.20				
14	GND	782.00	941.20				
15	P07	782.00	1021.20				
16	P06	782.00	1101.20				
17	P05	782.00	1181.20				
18	P04	782.00	1261.20				
19	GND	490.72	1317.80				
20	VDD	410.72	1317.80				
21	P03	330.72	1317.80				
22	P02	250.72	1317.80				
23	P01	170.72	1317.80				
24	P00	90.72	1317.80				



SNC82040B

CHIP SIZE: X=1650um, Y=2721um

Note: The substrate MUST be connected to GND in PCB layout.