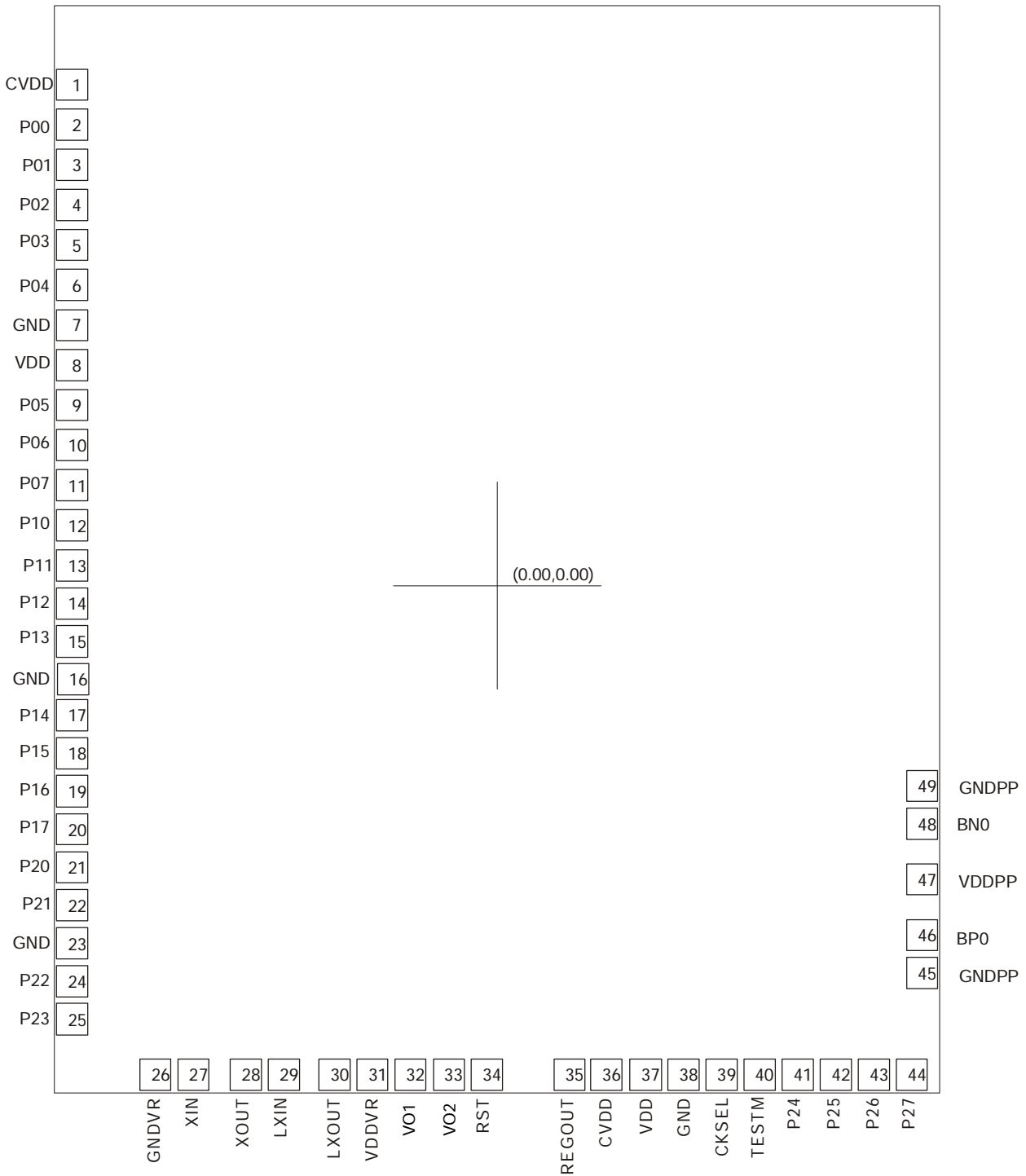




PROJECT: SNC88121A

NO	PAD NAME	X (um)	Y (um)	NO	PAD NAME	X (um)	Y (um)
1	CVDD	-1227.00	1400.81	26	GNDVR	-987.37	-1495.50
2	P00	-1227.00	1290.81	27	XIN	-877.37	-1495.50
3	P01	-1227.00	1180.81	28	XOUT	-725.77	-1495.50
4	P02	-1227.00	1070.81	29	LXIN	-615.77	-1495.50
5	P03	-1227.00	960.81	30	LXOUT	-470.17	-1495.50
6	P04	-1227.00	844.81	31	VDDVR	-360.17	-1495.50
7	GND	-1227.00	728.81	32	VO1	-250.17	-1495.50
8	VDD	-1227.00	612.81	33	VO2	-140.17	-1495.50
9	P05	-1227.00	496.81	34	RST	-29.67	-1495.50
10	P06	-1227.00	380.81	35	REGOUT	208.63	-1495.50
11	P07	-1227.00	270.81	36	CVDD	315.70	-1495.50
12	P10	-1227.00	160.81	37	VDD	428.63	-1495.50
13	P11	-1227.00	50.81	38	GND	538.63	-1495.50
14	P12	-1227.00	-59.19	39	CKSEL	648.63	-1495.50
15	P13	-1227.00	-169.19	40	TESTM	758.63	-1495.50
16	GND	-1227.00	-297.19	41	P24	868.63	-1495.50
17	P14	-1227.00	-407.19	42	P25	978.63	-1495.50
18	P15	-1227.00	-517.19	43	P26	1088.63	-1495.50
19	P16	-1227.00	-627.19	44	P27	1198.63	-1495.50
20	P17	-1227.00	-737.19	45	GNDPP	1199.00	-1189.99
21	P20	-1227.00	-847.19	46	BP0	1199.00	-1079.99
22	P21	-1227.00	-957.19	47	VDDPP	1199.00	-919.29
23	GND	-1227.00	-1067.19	48	BNO	1199.00	-758.59
24	P22	-1227.00	-1177.19	49	GNDPP	1199.00	-648.59
25	P23	-1227.00	-1287.19				



SNC88121A

CHIP SIZE: X=2558um, Y=3095um

Note: The substrate MUST be connected to GND in PCB layout.